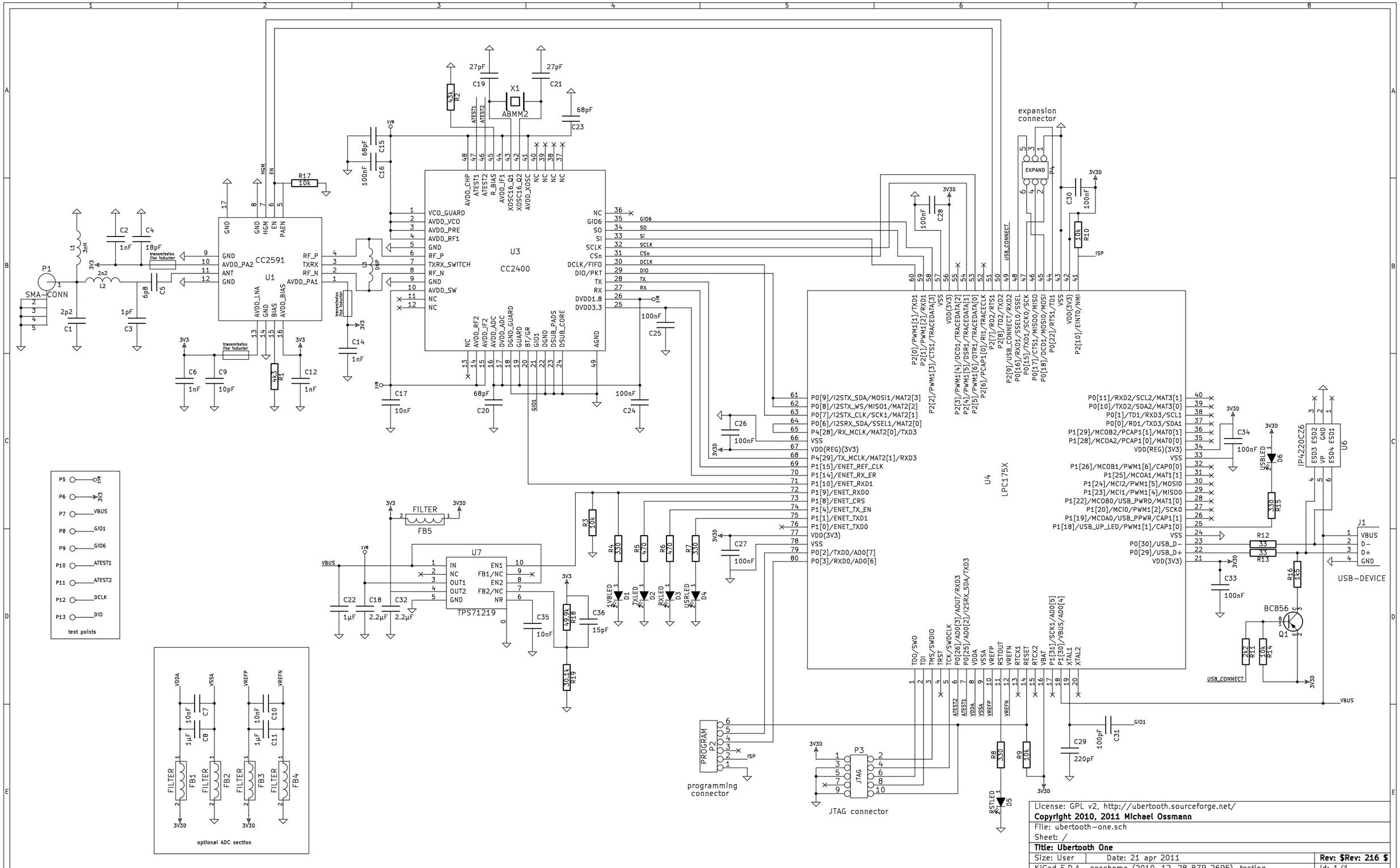
U1
CC2591
GND
AVDD_PA2
ANT
GND
RF_P
TXRX
RF_N
AVDD_PA1
AVDD_LNA
GND
BIAS
AVDD_BIAS
HGM
EN
PAEN
U3
CC2400
VCO_GUARD
AVDD_VCO
AVDD_PRE
AVDD_RF1
GND
RF_P
TXRX_SWITCH
RF_N
GND
AVDD_SW
NC
NC
AVDD_CHP
ATEST1
ATEST2
R_BIAS
AVDD_IF1
XOSC16_Q1
XOSC16_Q2
AVDD_XOSC
NC
GIO6
SO
SI
SCLK
CSn
DCLK/FIFO
DIO/PKT
TX
RX
DVDD1.8
DVDD3.3
AGND
NC
AVDD_RF2
AVDD_IF2
AVDD_ADC
DVDD_ADC
DGND_GUARD
GUARD
BT/GR
GIO1
DGND
DSUB_PADS
DSUB_CORE
X1
ABMM2
27pF
C19
27pF
C21
68pF
C23
68pF
C15
100nF
C16
R17
10k
4k3
R2
4k3
R1
P1
SMA-CONN
L1
3nH
L2
2n2
C1
2p2
C2
1nF
C4
18pF
C5
6p8
C3
1pF
C6
1nF
C9
10pF
C12
1nF
C14
1nF
C17
10nF
C20
68pF
C24
100nF
C25
100nF
C26
100nF
C27
100nF
C28
100nF
C30
100nF
C34
100nF
C33
100nF
C35
10nF
C36
15pF
C29
220pF
C31
100pF
C32
2.2µF
C18
2.2µF
C22
1µF
U7
TPS71219
IN
NC
OUT1
OUT2
GND
EN1
FB1/NC
EN2
FB2/NC
NR
FILTER
FB5
R3
10k
R4
330
R5
470
R6
470
R7
330
D1
D2
D3
D4
1V8LED
TXLED
RXLED
USRLED
R18
49.9k
R19
30.1k
U4
LPC175X
P0[9]/I2STX_SDA/MOSI1/MAT2[3]
P0[8]/I2STX_WS/MISO1/MAT2[2]
P0[7]/I2STX_CLK/SCK1/MAT2[1]
P0[6]/I2SRX_SDA/SSEL1/MAT2[0]
P4[28]/RX_MCLK/MAT2[0]/TXD3
VSS
VDD(REG)(3V3)
P4[29]/TX_MCLK/MAT2[1]/RXD3
P1[15]/ENET_REF_CLK
P1[14]/ENET_RX_ER
P1[10]/ENET_RXD1
P1[9]/ENET_RXD0
P1[8]/ENET_CRS
P1[4]/ENET_TX_EN
P1[1]/ENET_TXD1
P1[0]/ENET_TXD0
VDD(3V3)
VSS
P0[2]/TXD0/AD0[7]
P0[3]/RXD0/AD0[6]
P2[0]/PWM1[1]/TXD1
P2[1]/PWM1[2]/RXD1
P2[2]/PWM1[3]/CTS1/TRACEDATA[3]
VSS
VDD(3V3)
P2[3]/PWM1[4]/DCD1/TRACEDATA[2]
P2[4]/PWM1[5]/DSR1/TRACEDATA[1]
P2[5]/PWM1[6]/DTR1/TRACEDATA[0]
P2[6]/PCAP1[0]/RI1/TRACECLK
P2[7]/RD2/RTS1
P2[8]/TD2/TXD2
P2[9]/USB_CONNECT/RXD2
P0[16]/RXD1/SSEL0/SSEL1
P0[15]/TXD1/SCK0/SCK
P0[17]/CTS1/MISO0/MISO
P0[18]/DCD1/MOSI0/MOSI
P0[22]/RTS1/TD1
VSS
VDD(3V3)
P2[10]/EINT0/NMI
P0[11]/RXD2/SCL2/MAT3[1]
P0[10]/TXD2/SDA2/MAT3[0]
P0[1]/TD1/RXD3/SCL1
P0[0]/RD1/TXD3/SDA1
P1[29]/MCOB2/PCAP1[1]/MAT0[1]
P1[28]/MCOA2/PCAP1[0]/MAT0[0]
VDD(REG)(3V3)
VSS
P1[26]/MCOB1/PWM1[6]/CAP0[0]
P1[25]/MCOA1/MAT1[1]
P1[24]/MCI2/PWM1[5]/MOSI0
P1[23]/MCI1/PWM1[4]/MISO0
P1[22]/MCOB0/USB_PWRD/MAT1[0]
P1[20]/MCI0/PWM1[2]/SCK0
P1[19]/MCOA0/USB_PPWR/CAP1[1]
P1[18]/USB_UP_LED/PWM1[1]/CAP1[0]
VSS
P0[30]/USB_D-
P0[29]/USB_D+
VDD(3V3)
TDO/SWO
TDI
TMS/SWDIO
TRST
TCK/SWDCLK
P0[26]/AD0[3]/AOUT/RXD3
P0[25]/AD0[2]/I2SRX_SDA/TXD3
VDDA
VSSA
VREFP
RSTOUT
VREFN
RTCX1
RESET
RTCX2
VBAT
P1[31]/SCK1/AD0[5]
P1[30]/VBUS/AD0[4]
XTAL1
XTAL2
USB_CONNECT
expansion connector
EXPAND
P4
ISP
IP4220CZ6
ESD3
ESD2
VP
GND
ESD4
ESD1
U6
USBLED
D6
330
R15
J1
VBUS
D-
D+
GND
USB-DEVICE
R12
33
R13
33
R16
15
BC856
Q1
2k2
R11
10k
R14
USB_CONNECT
R8
330
R9
10k
RSTLED
D5
PROGRAM
P2
programming connector
P3
JTAG
JTAG connector
test points
P5
P6
P7
P8
P9
P10
P11
P12
P13
1V8
3V3
VBUS
GIO1
GIO6
ATEST1
ATEST2
DCLK
DIO
FILTER
FB1
FB2
FB3
FB4
VDDA
VSSA
VREFP
VREFN
10nF
C7
1µF
C8
10nF
C10
1µF
C11
optional ADC section
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Size: User
Date: 21 apr 2011
Rev: $Rev: 216 $
KiCad E.D.A. eeschema (2010-12-28 BZR 2695)-testing
Id: 1/1