* Two MPU-60X0s can be connected to the same I2C bus.
  The LSB bit of the 7-bit address is determined by the logic level on pin ADD.
  Default Address = 0x6B (pin ADD is logic low).
  Alternative Address = 0x69 (pin ADD is logic high).

** Optional external reference clock input. Connected to GND by default.
  Cut trace for external clock

*** Frame synchronization digital input. Connected to GND by default
  Cut trace for external sync

** ** ** ** **

** TITLE:** MPU-6050_Breakout V11
** Document Number:**
** REVISION:**
** Date:** not saved!
** Sheet:** 1/1